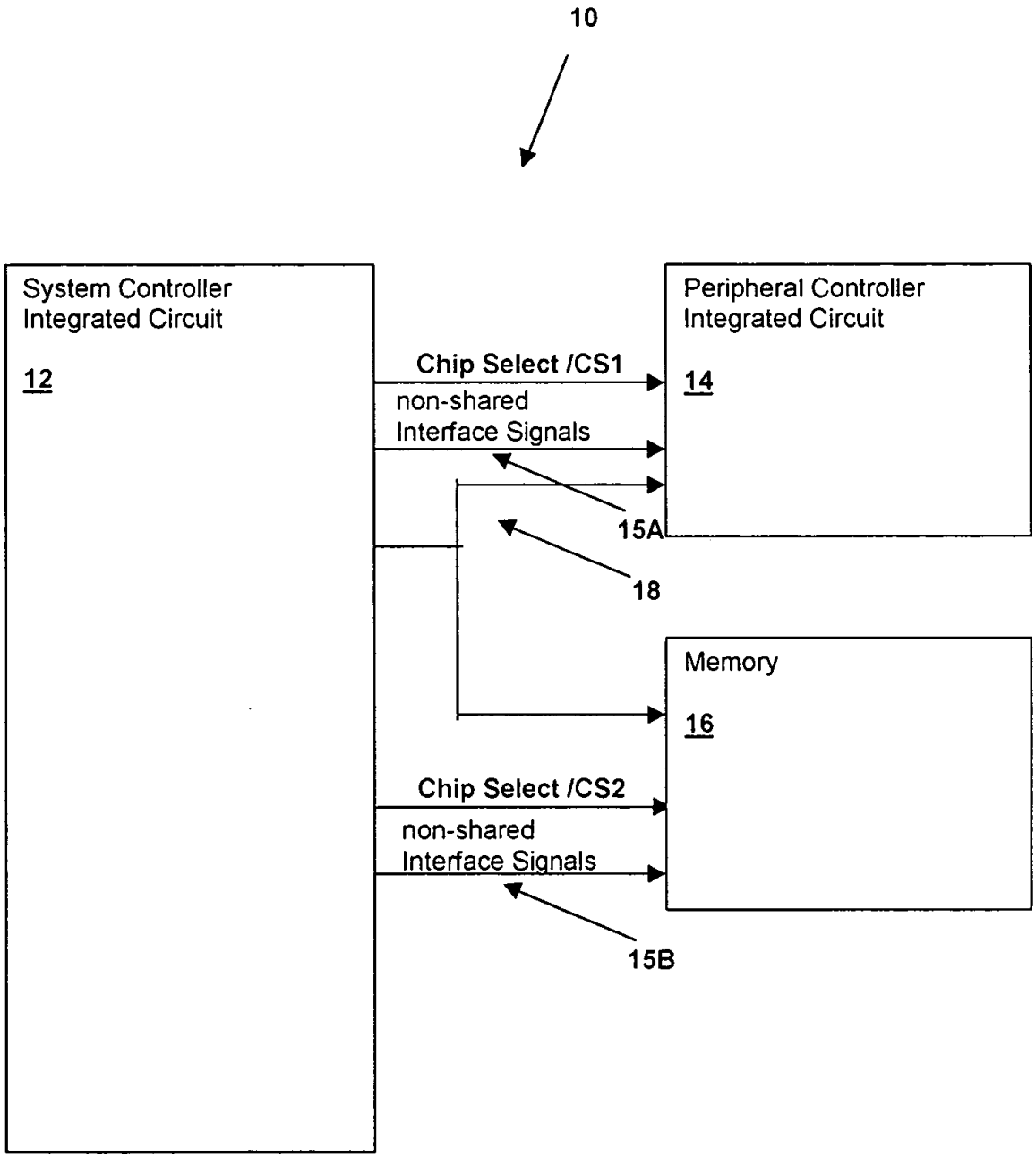
10
System Controller
Integrated Circuit
12
Peripheral Controller
Integrated Circuit
14
Chip Select /CS1
non-shared
Interface Signals
15A
18
Memory
16
Chip Select /CS2
non-shared
Interface Signals
15B

Fig. 1

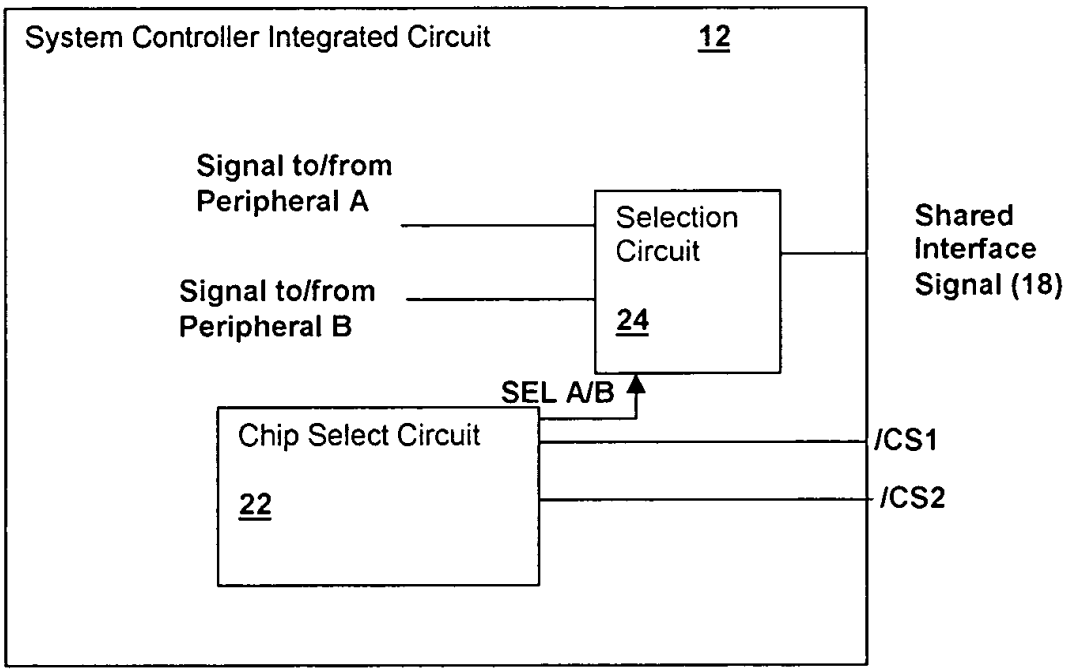
System Controller Integrated Circuit
12
Signal to/from Peripheral A
Signal to/from Peripheral B
Selection Circuit
24
Shared Interface Signal (18)
SEL A/B
Chip Select Circuit
22
/CS1
/CS2

Fig. 2

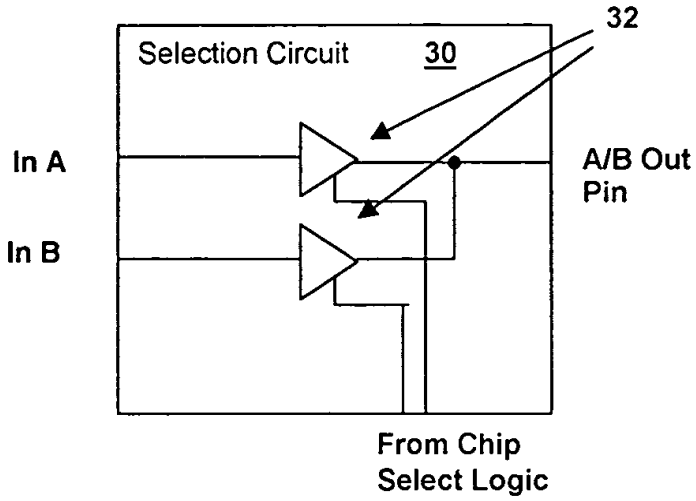

Fig. 3A

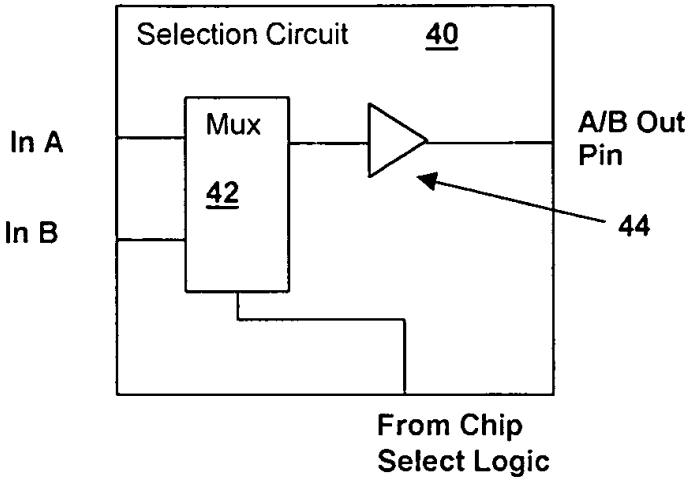

Fig. 3B

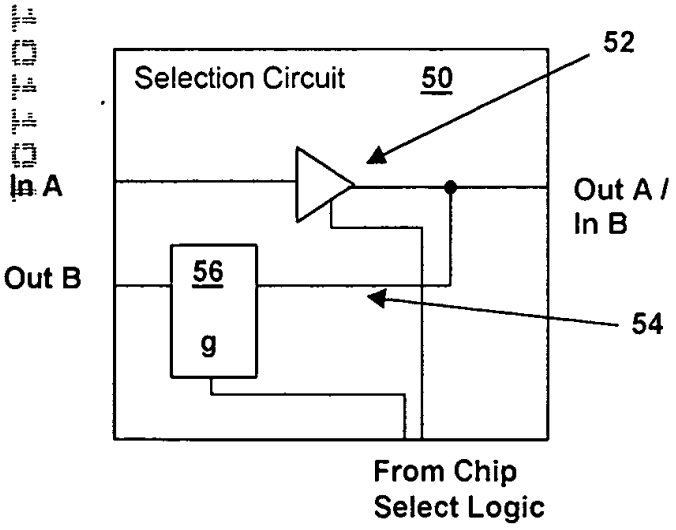

Fig. 3C

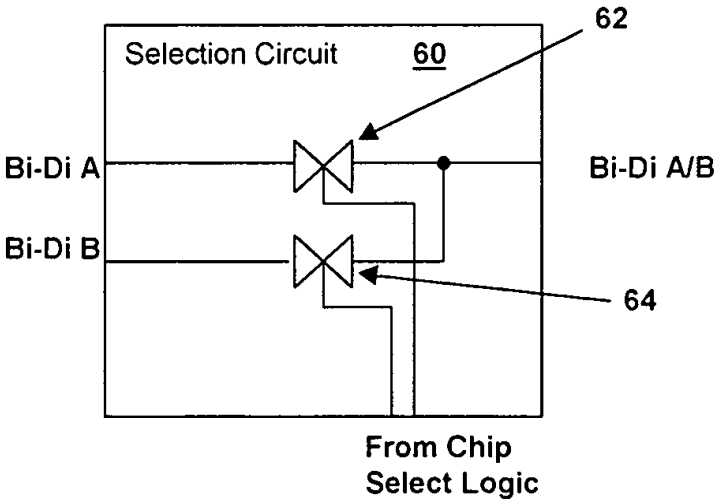

Fig. 3D

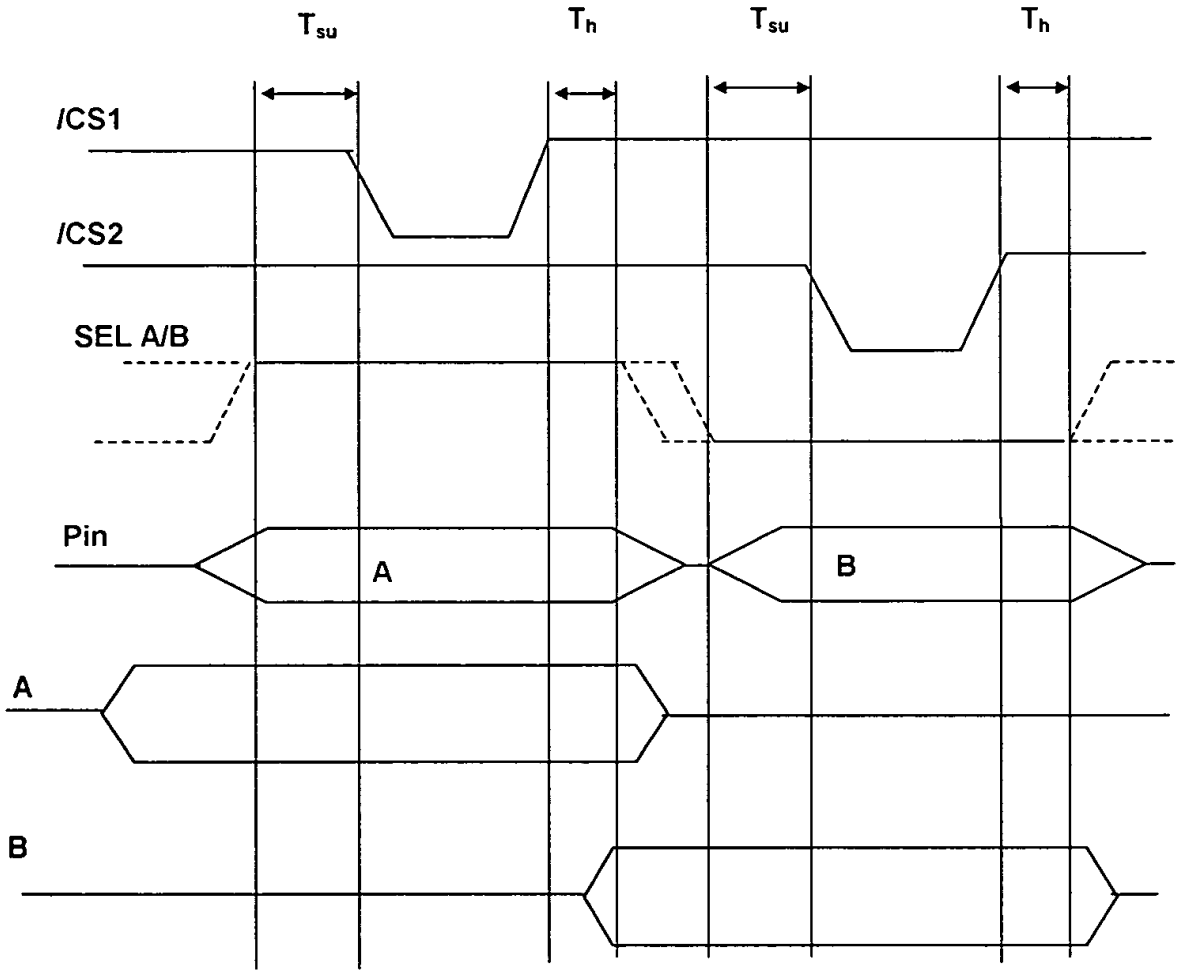

Fig. 4